

Figure 1

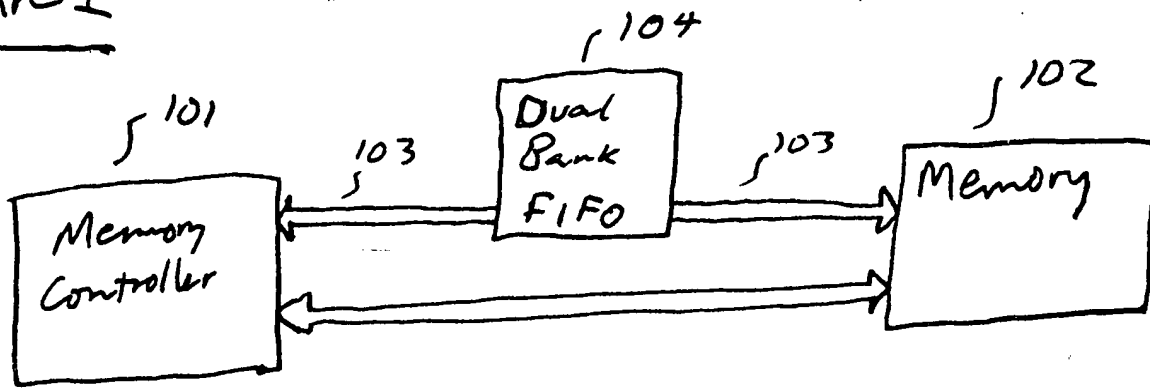


FIGURE 2

Dual Bank FIFO Buffer (Receiver) Block Diagram

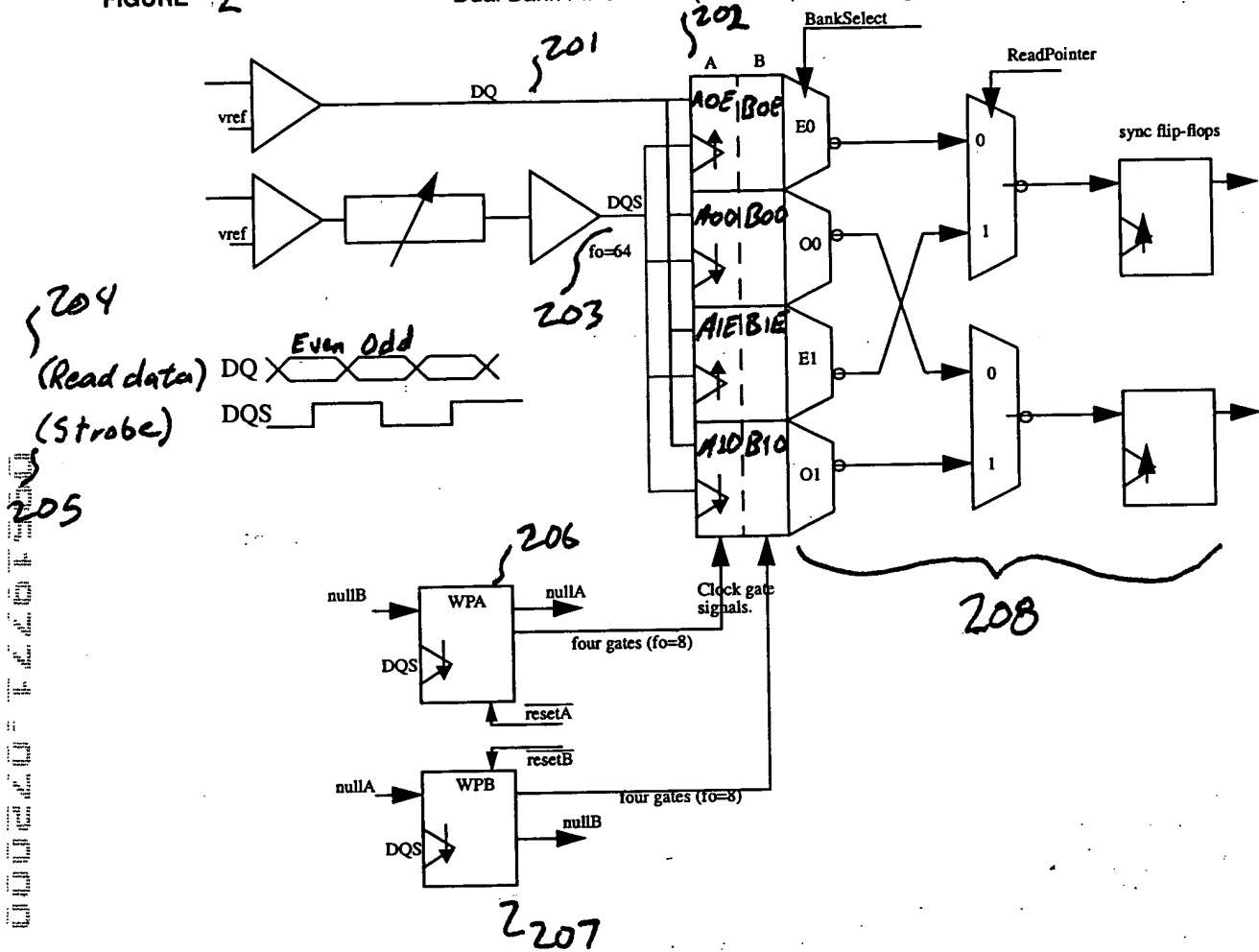


FIGURE 3

FIFO Valid Data Windows

